

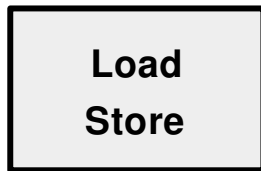
① bndmk   ② bndcl/bndcu   ③ bndmov   ④ bndldx   ⑤ bndstx

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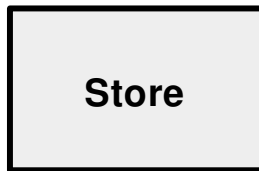
port 0



port 2



port 4



port 6



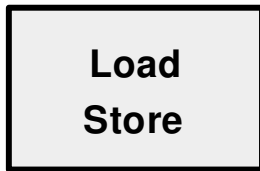
① ② ③ ④

port 1



③ ④ ⑤

port 3



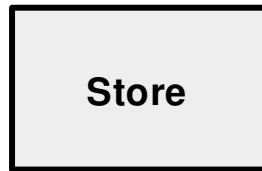
③ ⑤

port 5



① ② ③ ④

port 7



① ② ③ ④ ⑤

③ ④ ⑤

① ③ ④

③ ⑤